

Notice of References Cited	Application/Control No. 10/619,884	Applicant(s)/Patent Under Reexamination MALDEI ET AL.	
	Examiner Brook Kebede	Art Unit 2823	Page 1 of 1

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	C	US-6,236,079	05-2001	Nitayama et al.	257/306
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NON-PATENT DOCUMENTS

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X	U	Wolf et al., Silicon Processing for The VLSI Era, Volume 1: Process Technology, Pp. 184, (1986).
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*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
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